Early Evaluation of a New Vector Processor SX-Aurora TSUBASA

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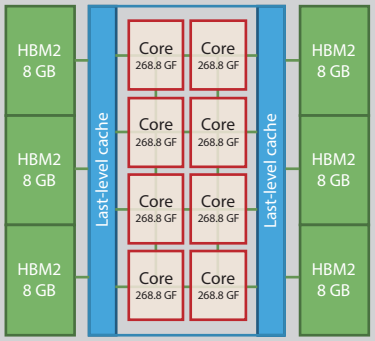
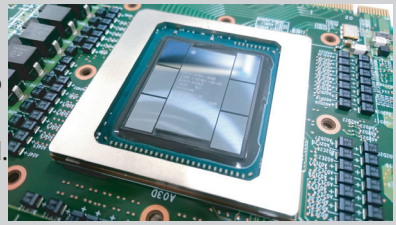
Overview of SX-Aurora TSUBASA

SX-Aurora TSUBASA vector processor can provide a high memory bandwidth for practical HPC applications.

- **High computational performance**
  by a 8-core vector processor
- **High sustained memory bandwidth**
  by six HBM2 memory modules integration
- **High usability**
  by a new execution model that an application is executed on a Vector Engine (**VE**) while system calls are offloaded to a Vector Host(**VH**)





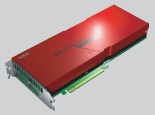
CPU·Memory·LLC Implementation

- The processor is manufactured with 16 nm FINFET process technology
- About 4.8 billion transistors are integrated into an area of 14.96 mm by 33.0 mm.
- Six HBM2s are firstly implemented in the world.
- LLC at both side of cores is connected to each core through 2D mesh network

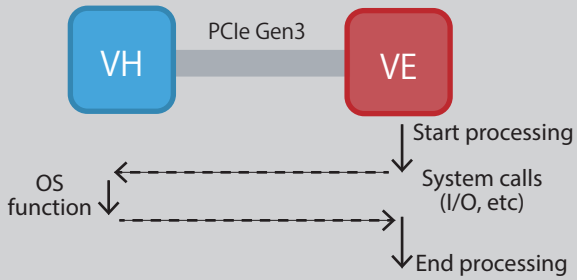
SX-Aurora TSUBASA concept: Easy to USE

- Standard Linux environment
- Standard Fortran/C/C++ languages
- Automatic vectorization / OpenMP
- Automatic parallelization

→ **x86 node (VH) is attached to VE**

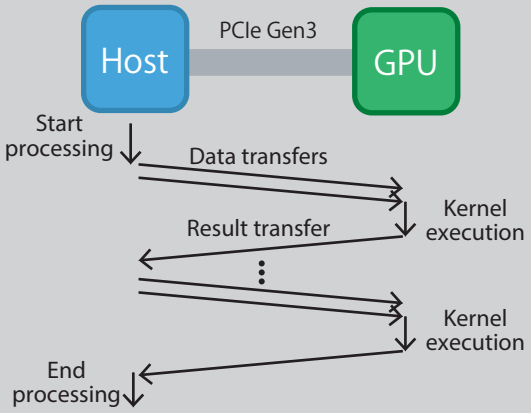


SX-Aurora TSUBASA execution model



Data transfer bottleneck among VH and VE can be avoided

Conventional accelerator execution model



Data transfers easily become bottleneck

Experimental Environments

Vector Engine: SX-Aurora TSUBASA Type 10B

- Peak performance	: 2.15 TFlop/s
- CPU Frequency	: 1.4 GHz
- Memory bandwidth	: 1.22 TB/s
- Memory capacity	: 48 GB
- LLC bandwidth	: 3.0 TB/s
- Maximum vector length	: 256

Vector Host: Intel Xeon Gold 6126

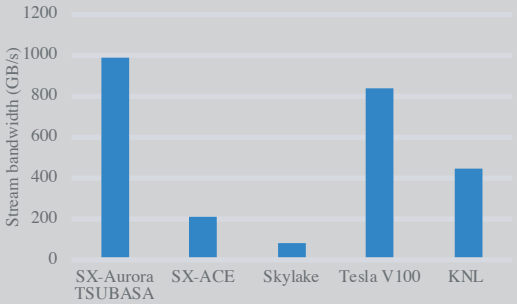
- Peak Performance	: 998.4 / 1420 GFlop/s
- CPU frequency	: 2.6 / 3.7 GHz
- Memory bandwidth	: 128 GB/s
- Memory capacity	: 96 GB
- Maximum AVX length	: 8

Benchmark programs
- Stream benchmark (Triad)
- Himeno benchmark

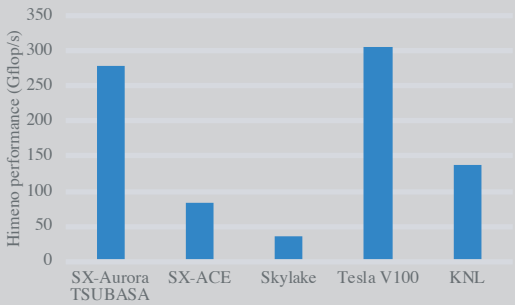
Kernels of in-house applications
- Land mine: 3D FDTD (6.22 Bytes/Flop)
- Earthquake: Ocean plates (6.00 B/F)
- Plasma: Lax-Wendroff (3.02 B/F)
- Turbulent flow: CFD, NS equ. (1.91 B/F)
- Antenna: 3D FDTD (1.73 B/F)
- Turbine: CFD, LU-SGS (1.14 B/F)

Software environments
- CentOS Linux 7.3.1611
- VEOS 1.0.3
- NEC Fortran compiler 1.2.0
- NEC C/C++ compiler 1.2.0

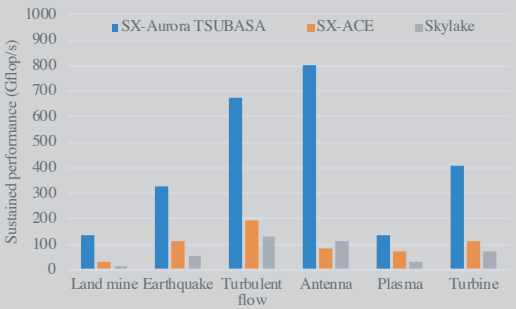
Performance Evaluation



SX-Aurora TSUBASA achieves 4.5, 11.3, 1.2, and 2.2 times higher sustained memory bandwidth than those of SX-ACE, Xeon 6126, Tesla V100, Xeon Phi KNL 7290, respectively.



SX-Aurora TSUBASA achieves 2.3, 5.4, 1.8, and 2.0 times higher performance than those of SX-ACE, Xeon 6126, Tesla V100, Xeon Phi KNL 7290, respectively.



SX-Aurora TSUBASA achieves about 1.97 to 9.75 times faster than SX-ACE due to its high computational capability and high sustained memory bandwidth.

Conclusions

SX-Aurora TSUBASA has a high potential to accelerate various applications by its vector computational capability and high sustained memory bandwidth. The balance between memory performance and core performance is important to achieve high sustained performance.